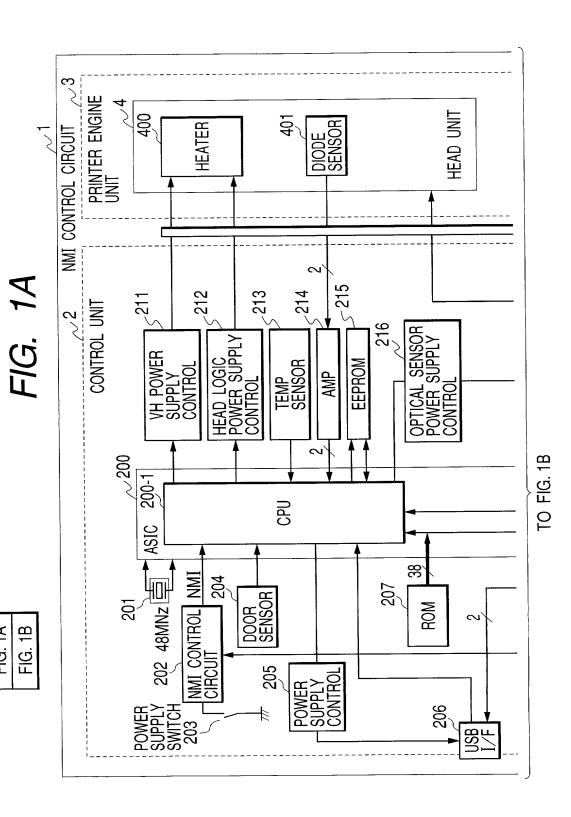
FIG. 1A





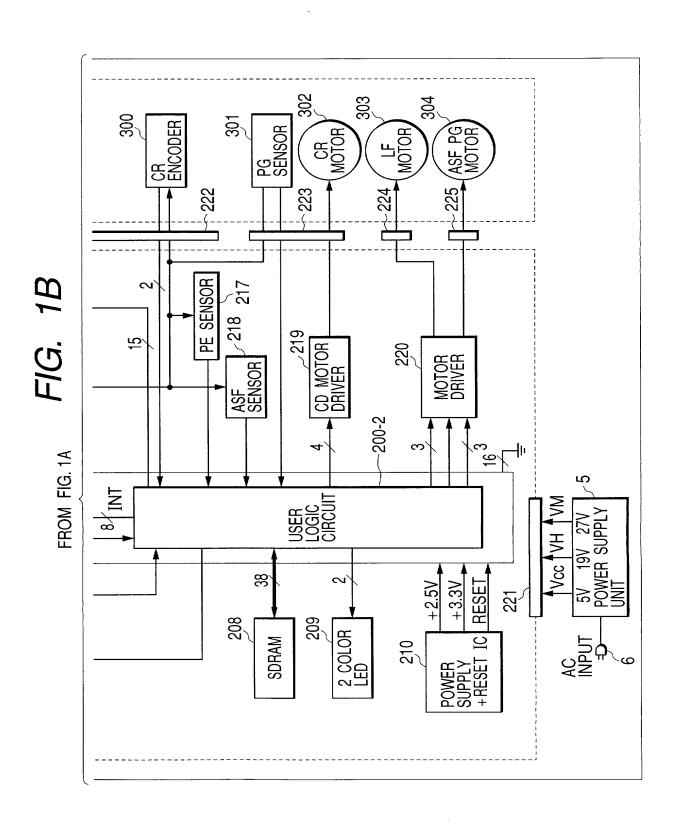


FIG. 2

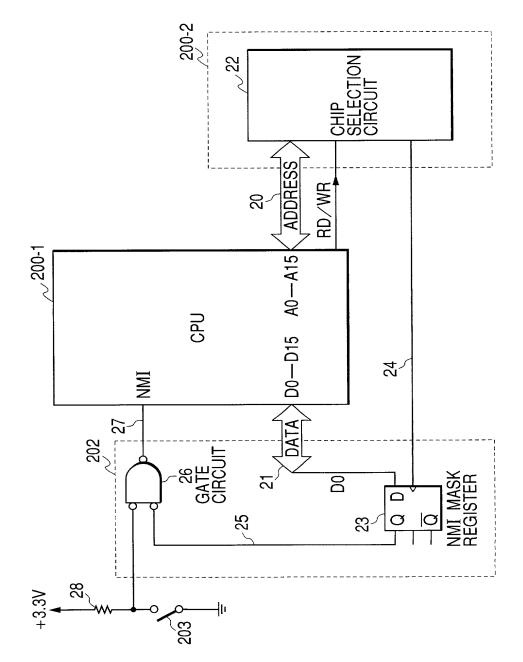
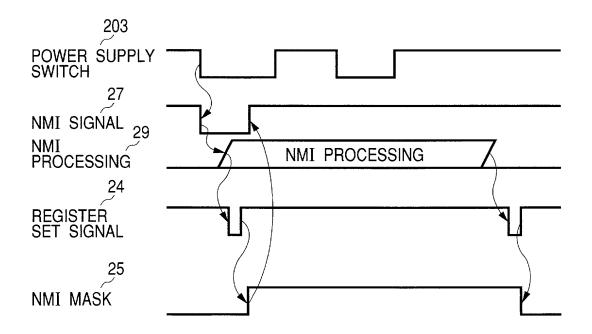


FIG. 3



## FIG. 4A

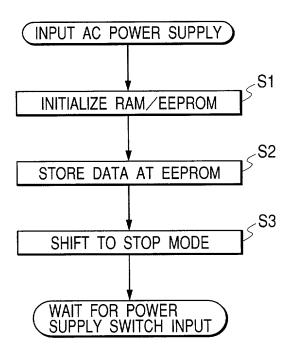


FIG. 4B

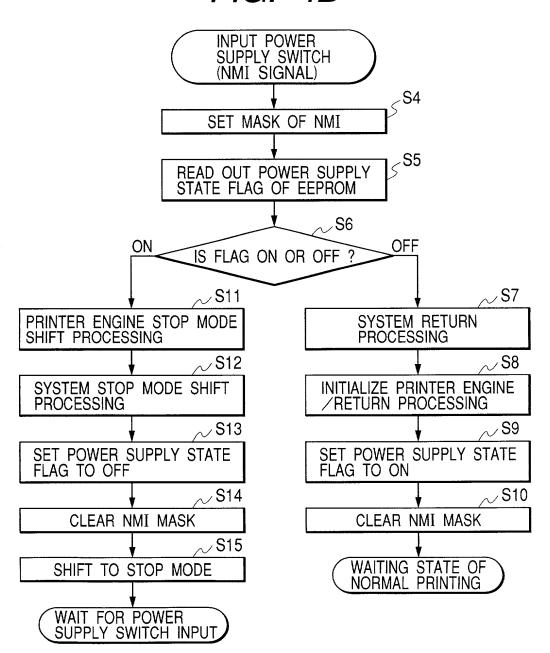
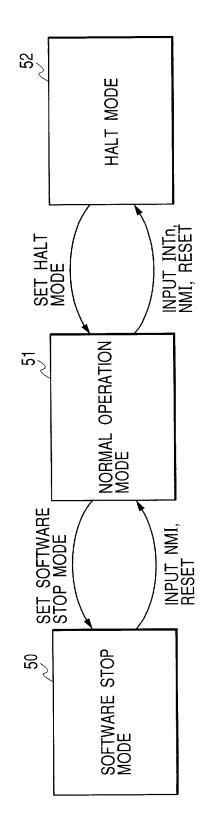


FIG. 5



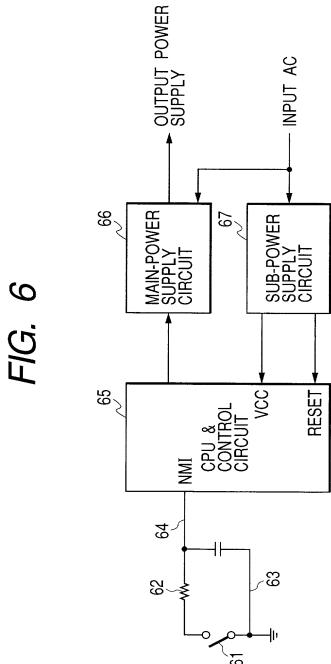
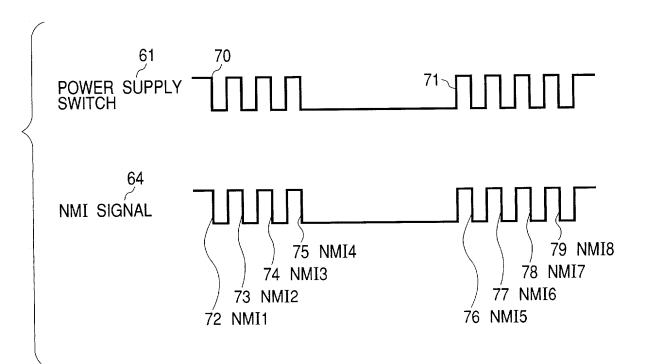


FIG. 7



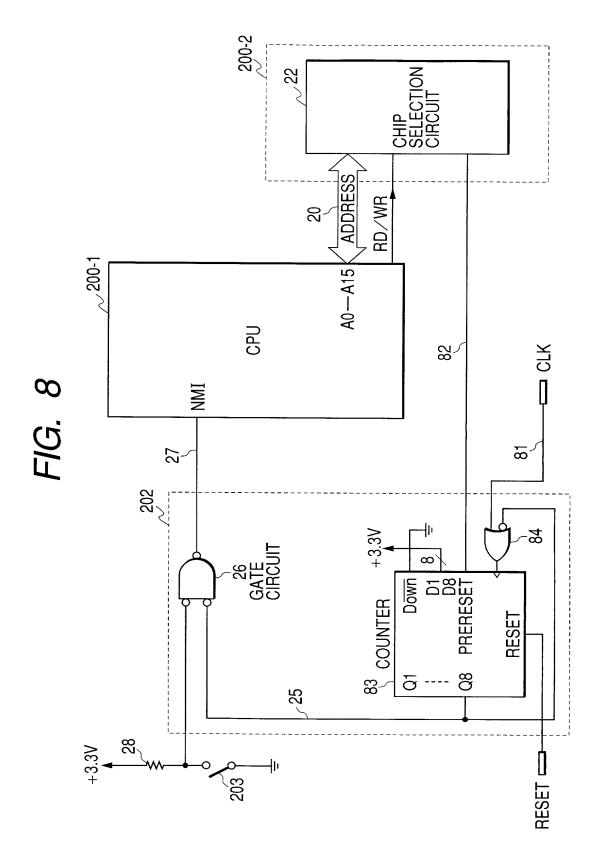
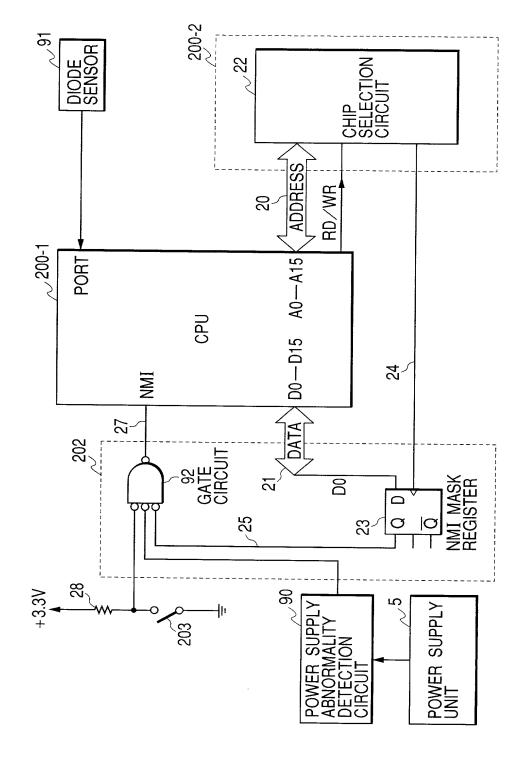
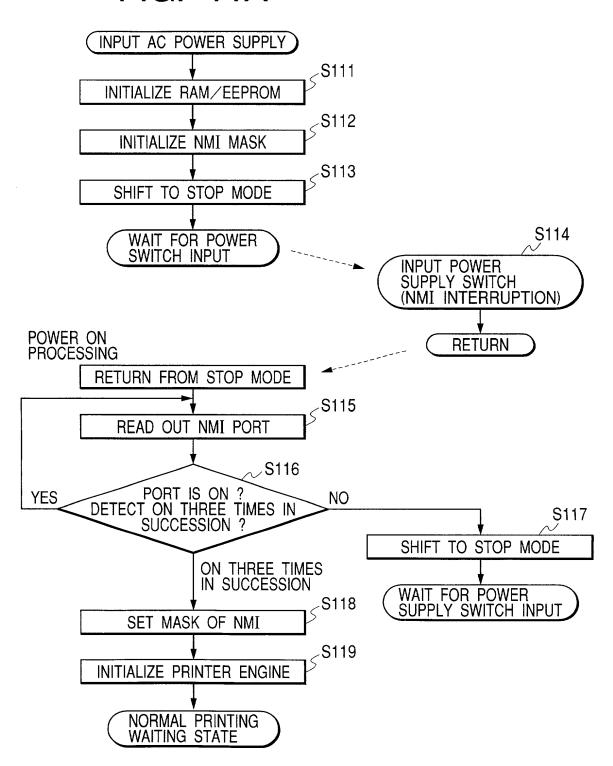


FIG. 9



200-2 50 200-1 D0-D15 A0-A15 FIG. 10 CPU PORT0 5<del>4</del> IM 27 205 202 8 4-Ø **8**/

FIG. 11A



## FIG. 11B

